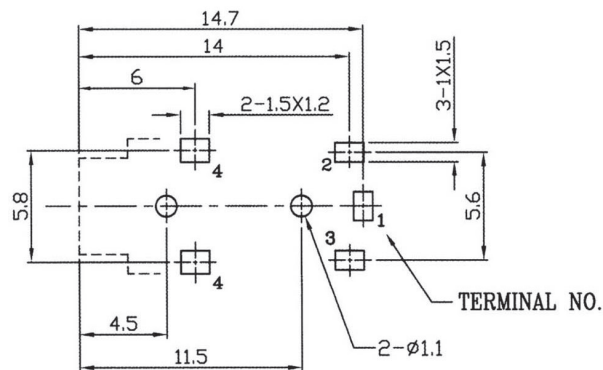
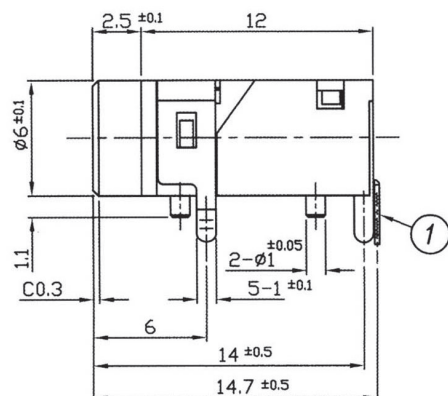
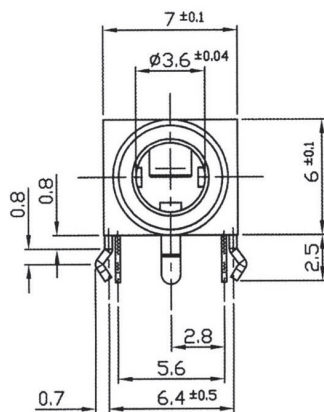
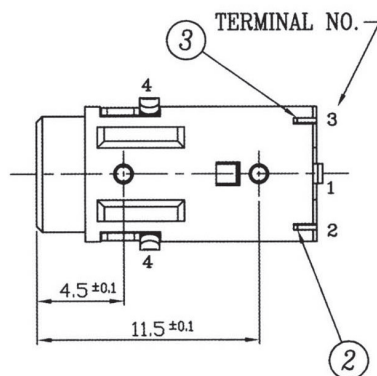
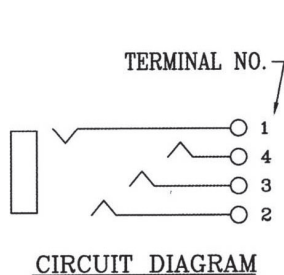


SPECIFICATION

1. WITHSTANDING VOLTAGE : 500V AC FOR 1MINUTE
2. INSULATION RESISTANCE: 100MΩ MIN. AT 500V DC
3. CONTACT RESISTANCE: 30mΩ MAX



PC BOARD HOLE LAYOUT (Tols:±0.1)
(VIEW FROM DIRECTION OF INSERTION TO PCB)



5	BODY	1	PA66/GF RESIN	BLACK
4	CHIP SPRING	1	PBs 0.4t	Ag-PLATED
3	RING SPRING #3	1	PBs 0.25t	Ag-PLATED
2	RING SPRING #2	1	PBs 0.25t	Ag-PLATED
1	SLEEVE SPRING	1	PBs 0.25t	Ag-PLATED
LET	PART NAME	QTY	MATERIAL	REMARK

TOLERANCES	PART NAME	DWG NO. VER.1
X ≤ 5 = ± 0.2	φ3.5 EARPHONE JACK	
X > 5 = ± 0.3	MATL	SCALE
ANGULAR = ± ---	AS SHOWN IN TABLE	3/1 mm
DO NOT SCALE DWG.	DATE	DRN / APPD.
	16-08-2010	Y.H.TSANG/K.F.IM
FINISHED		



Made in Japan

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